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jonathanpwang committed 2 weeks ago

fix: auipc range check pc_limbs[3] to 6-bits

The order of enumerate and skip was wrong, causing the if statement to not trigger.

main · v1.1.0

1 parent [f41640c](#) commit 68da4b5

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- extensions/rv32im/circuit/src/auipc
 - core.rs

1 file changed +3 -2 lines changed Search within code

extensions/rv32im/circuit/src/auipc/core.rs +3 -2

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129

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242

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@@ -129,8 +129,9 @@ where

need_range_check.push(limb.into());

}

+ assert_eq!(pc_limbs.len(), RV32_REGISTER_NUM_LIMBS);

// pc_limbs[0] is already range checked through rd_data[0]

- for (i, limb) in pc_limbs.iter().skip(1).enumerate() {

+ for (i, limb) in pc_limbs.iter().enumerate().skip(1) {

if i == pc_limbs.len() - 1 {

// Range check the most significant limb of pc to be in [0, 2^{PC_BITS-

(RV32_REGISTER_NUM_LIMBS-1)*RV32_CELL_BITS})

need_range_check.push(

@@ -242,7 +243,7 @@ where

need_range_check.push(limb);

}

- for (i, limb) in pc_limbs.iter().skip(1).enumerate() {

+ for (i, limb) in pc_limbs.iter().enumerate().skip(1) {

```
246 247         if i == pc_limbs.len() - 1 {
247 248             need_range_check.push(*limb) << (pc_limbs.len() * RV32_CELL_BITS -
PC_BITS));
248 249         } else {
.....
↓
```

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