



author Krishna chaitanya chundru <quic_krichai@quicinc.com> 2023-02-28 17:19:12 +0530
committer Greg Kroah-Hartman <gregkh@linuxfoundation.org> 2023-03-30 12:49:29 +0200
commit 267b899375bf38944d915c9654d6eb434edad0ce (patch)
tree 46a6e35301c94bee7c3b5ad61c7c08b3e15b2079
parent 0156ad2a60fe92373164399fc9a66976db4e60d1 (diff)
download linux-267b899375bf38944d915c9654d6eb434edad0ce.tar.gz

diff options

context: 3 ▼
space: include ▼
mode: unified ▼

arm64: dts: qcom: sc7280: Mark PCIe controller as cache coherent

commit 8a63441e83724fee1ef3fd37b237d40d90780766 upstream.

If the controller is not marked as cache coherent, then kernel will try to ensure coherency during dma-ops and that may cause data corruption. So, mark the PCIe node as dma-coherent as the devices on PCIe bus are cache coherent.

Cc: stable@vger.kernel.org

Fixes: 92e0ee9f83b3 ("arm64: dts: qcom: sc7280: Add PCIe and PHY related node")

Signed-off-by: Krishna chaitanya chundru <quic_krichai@quicinc.com>

Signed-off-by: Bjorn Andersson <andersson@kernel.org>

Link: https://lore.kernel.org/r/1677584952-17496-1-git-send-email-quic_krichai@quicinc.com

Signed-off-by: Greg Kroah-Hartman <gregkh@linuxfoundation.org>

Diffstat

```
-rw-r--r-- arch/arm64/boot/dts/qcom/sc7280.dtsi 2
```

1 files changed, 2 insertions, 0 deletions

diff --git a/arch/arm64/boot/dts/qcom/sc7280.dtsi b/arch/arm64/boot/dts/qcom/sc7280.dtsi

index 516e70bf04ce96..346da6af51ac91 100644

--- a/arch/arm64/boot/dts/qcom/sc7280.dtsi

+++ b/arch/arm64/boot/dts/qcom/sc7280.dtsi

@@ -2077,6 +2077,8 @@

```
pinctrl-names = "default";  
pinctrl-0 = <&pcie1_clkreq_n>;
```

+ dma-coherent;

+ iommus = <&apps_smmu 0x1c80 0x1>;

```
iommu-map = <0x0 &apps_smmu 0x1c80 0x1>;
```