



# index : kernel/git/stable/linux.git

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Stable Group

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committer Greg Kroah-Hartman <gregkh@linuxfoundation.org> 2023-03-30 12:49:13 +0200  
commit [ab33a8f7649b0324639a336e1081aeea51a4523e](#) (patch)  
tree [f9275a21c26785b95a6e232ac483edb72966e7ae](#)  
parent [adfc7aaa0df91e567f8f01f6066f385328d32e86](#) (diff)  
download [linux-ab33a8f7649b0324639a336e1081aeea51a4523e.tar.gz](#)

## diff options

context:    
space:    
mode:

## perf/x86/amd/core: Always clear status for idx

[ Upstream commit 263f5ecaf7080513efc248ec739b6d9e00f4129f ]

The variable 'status' (which contains the unhandled overflow bits) is not being properly masked in some cases, displaying the following warning:

```
WARNING: CPU: 156 PID: 475601 at arch/x86/events/amd/core.c:972 amd_pmu_v2_handle_irq+0x216/0x270
```

This seems to be happening because the loop is being continued before the status bit being unset, in case x86\_perf\_event\_set\_period() returns 0. This is also causing an inconsistency because the "handled" counter is incremented, but the status bit is not cleaned.

Move the bit cleaning together above, together when the "handled" counter is incremented.

Fixes: 7685665c390d ("perf/x86/amd/core: Add PerfMonV2 overflow handling")  
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Signed-off-by: Peter Zijlstra (Intel) <peterz@infradead.org>  
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Signed-off-by: Sasha Levin <sasha@kernel.org>

## Diffstat

```
-rw-r--r-- arch/x86/events/amd/core.c 3
```

1 files changed, 1 insertions, 2 deletions

**diff --git a/arch/x86/events/amd/core.c b/arch/x86/events/amd/core.c**

**index 4386b10682ce4f..8ca5e827f30b2c 100644**

**--- a/arch/x86/events/amd/core.c**

**+++ b/arch/x86/events/amd/core.c**

**@@ -923,6 +923,7 @@ static int amd\_pmu\_v2\_handle\_irq(struct pt\_regs \*regs)**

**/\* Event overflow \*/**

**handled++;**

**+ status &= ~mask;**

**perf\_sample\_data\_init(&data, 0, hwc->last\_period);**

**if (!x86\_perf\_event\_set\_period(event))**

**@@ -935,8 +936,6 @@ static int amd\_pmu\_v2\_handle\_irq(struct pt\_regs \*regs)**

**if (perf\_event\_overflow(event, &data, regs))**

```
- x86_pmu_stop(event, 0);  
-  
- status &= ~mask;  
-  
}  
  
/*
```