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committer Peter Zijlstra <peterz@infradead.org> 2023-03-21 14:43:05 +0100  
commit 263f5ecaf7080513efc248ec739b6d9e00f4129f ([patch](#))  
tree 1f5767b26fc992f0984122f54d090087fed19de8  
parent e8d018dd0257f744ca50a729e3d042cf2ec9da65 ([diff](#))  
download [linux-263f5ecaf7080513efc248ec739b6d9e00f4129f.tar.gz](#)

**diff options**

context:	<input type="button" value="3"/>
space:	<input type="button" value="include"/>
mode:	<input type="button" value="unified"/>

**perf/x86/amd/core: Always clear status for idx**

The variable 'status' (which contains the unhandled overflow bits) is not being properly masked in some cases, displaying the following warning:

WARNING: CPU: 156 PID: 475601 at arch/x86/events/amd/core.c:972 amd\_pmu\_v2\_handle\_irq+0x216/0x270

This seems to be happening because the loop is being continued before the status bit being unset, in case x86\_perf\_event\_set\_period() returns 0. This is also causing an inconsistency because the "handled" counter is incremented, but the status bit is not cleaned.

Move the bit cleaning together above, together when the "handled" counter is incremented.

Fixes: 7685665c390d ("perf/x86/amd/core: Add PerfMonV2 overflow handling")  
Signed-off-by: Breno Leitao <leitao@debian.org>  
Signed-off-by: Peter Zijlstra (Intel) <peterz@infradead.org>  
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Link: <https://lore.kernel.org/r/20230321113338.1669660-1-leitao@debian.org>

**Diffstat**

-rw-r--r-- arch/x86/events/amd/core.c 3

1 files changed, 1 insertions, 2 deletions

```
diff --git a/arch/x86/events/amd/core.c b/arch/x86/events/amd/core.c
index 8c45b198b62f55..bccea57dee81ec 100644
--- a/arch/x86/events/amd/core.c
+++ b/arch/x86/events/amd/core.c
@@ -923,6 +923,7 @@ static int amd_pmu_v2_handle_irq(struct pt_regs *regs)

     /* Event overflow */
     handled++;
+    status &= ~mask;
     perf_sample_data_init(&data, 0, hwc->last_period);

     if (!x86_perf_event_set_period(event))
@@ -933,8 +934,6 @@ static int amd_pmu_v2_handle_irq(struct pt_regs *regs)

         if (perf_event_overflow(event, &data, regs))
             x86_pmu_stop(event, 0);

-
-        status &= ~mask;
```

}

/\*

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