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author Roman Li <roman.li@amd.com> 2022-11-03 17:33:07 -0400
committer Greg Kroah-Hartman <gregkh@linuxfoundation.org> 2022-11-26 09:27:43 +0100
commit [f67ef5aa88e3db0a13ae3befab2ddf14ac00a91c](#) (patch)
tree [2e4284af70dec39185d88ebf1cfba87ce0b08204](#)
parent [2c323f5a8fb8bee604b827e2b0bfbb1480379a18](#) (diff)
download [linux-f67ef5aa88e3db0a13ae3befab2ddf14ac00a91c.tar.gz](#)

diff options

context: 3 ▼
space: include ▼
mode: unified ▼

drm/amd/display: Fix optc2_configure warning on dcn314

commit e7e4f77c991c9abf90924929a9d55f90b0bb78de upstream.

[Why]

dcn314 uses optc2_configure_crc() that wraps
optc1_configure_crc() + set additional registers
not applicable to dcn314.
It's not critical but when used leads to warning like:
WARNING: drivers/gpu/drm/amd/amdgpu/./display/dc/dc_helper.c
Call Trace:
<TASK>
generic_reg_set_ex+0x6d/0xe0 [amdgpu]
optc2_configure_crc+0x60/0x80 [amdgpu]
dc_stream_configure_crc+0x129/0x150 [amdgpu]
amdgpu_dm_crtc_configure_crc_source+0x5d/0xe0 [amdgpu]

[How]

Use optc1_configure_crc() directly

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Diffstat

-rw-r--r-- drivers/gpu/drm/amd/display/dc/dcn314/dcn314_optc.c 2

1 files changed, 1 insertions, 1 deletions

```
diff --git a/drivers/gpu/drm/amd/display/dc/dcn314/dcn314_optc.c b/drivers/gpu/drm/amd/display/dc/dcn314/dcn314_optc.c
index 38aa28ec6b1305..9c95ad14542059 100644
--- a/drivers/gpu/drm/amd/display/dc/dcn314/dcn314_optc.c
+++ b/drivers/gpu/drm/amd/display/dc/dcn314/dcn314_optc.c
@@ -237,7 +237,7 @@ static struct timing_generator_funcs dcn314_tg_funcs = {
     .clear_optc_underflow = optc1_clear_optc_underflow,
     .setup_global_swap_lock = NULL,
     .get_crc = optc1_get_crc,
-    .configure_crc = optc2_configure_crc,
+    .configure_crc = optc1_configure_crc,
     .set_dsc_config = optc3_set_dsc_config,
     .get_dsc_status = optc2_get_dsc_status,
     .set_dwb_source = NULL,
```